

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	0	cache\$2 near4 (poll\$4 or monitor\$6) near4 timestamp\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/21 21:04
S2	36	cache\$2 same (poll\$4 or monitor\$6) same timestamp\$2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/21 21:04
S3	1985324	frequenc\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/21 21:04
S4	12	S2 and S3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/21 21:05
S5	2	"6687793".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/21 21:05
S6	21821081	@ad<"20011228"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/21 21:06
S7	5	S4 and S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/21 21:34
S8	14	S2 and S6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/09/21 21:34

## EAST Search History

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S1	8	cache adj polling	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/09/05 11:33
S2	4548	timestamp	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/09/05 11:29
S3	90	timestamp near2 modif\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/09/05 11:29
S4	1711	active adj status	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/09/05 11:30
S5	593	polling adj interval	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/09/05 11:30
S6	138458	inactive	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/09/05 11:30
S7	324	first adj schedule	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/09/05 11:31
S8	269	second adj schedule	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/09/05 11:31
S9	169	cache adj optimiz\$6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/09/05 11:32

## EAST Search History

S10	0	S1 and S2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/09/05 11:32
S11	0	S1 and S4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/09/05 11:32
S12	0	S3 and S4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/09/05 11:32
S13	0	S1 and S9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/09/05 11:33



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### 1 [Cache management algorithms for flexible filesystems](#)



Silvano Maffeis

December 1993 **ACM SIGMETRICS Performance Evaluation Review**, Volume 21 Issue 2

**Publisher:** ACM Press

Full text available: [pdf\(611.92 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Cache management in flexible filesystems deals with the problem of determining a cached file to be replaced when the local cachespace is exhausted. In analogy to virtual memory management, several different algorithms exist for managing cached files. In this paper we simulate the behavior of *First-In-First-Out (FIFO)*, *Least Recently Used (LRU)*, *Least Frequently Used (LFU)* and a variation of LFU we call the *File Length Algorithm (LEN)* from the viewpoint of file access times, cache h ...

### 2 [On improving the performance of cache invalidation in mobile environments](#)



Guohong Cao

August 2002 **Mobile Networks and Applications**, Volume 7 Issue 4

**Publisher:** Kluwer Academic Publishers

Full text available: [pdf\(242.20 KB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#)

Many cache management schemes designed for mobile environments are based on invalidation reports (IRs). However, IR-based approach suffers from long query latency and it cannot efficiently utilize the broadcast bandwidth. In this paper, we propose techniques to address these problems. First, by replicating a small fraction of the essential information related to cache invalidation, the query latency can be reduced. Then, we propose techniques to efficiently utilize the broadcast bandwidth based ...

**Keywords:** counter, failure recovery, invalidation report, mobile computing, query latency

### 3 [The STAMPede approach to thread-level speculation](#)



J. Gregory Steffan, Christopher Colohan, Antonia Zhai, Todd C. Mowry

August 2005 **ACM Transactions on Computer Systems (TOCS)**, Volume 23 Issue 3

**Publisher:** ACM Press

Full text available: [pdf\(1.72 MB\)](#) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Multithreaded processor architectures are becoming increasingly commonplace: many

current and upcoming designs support chip multiprocessing, simultaneous multithreading, or both. While it is relatively straightforward to use these architectures to improve the throughput of a multithreaded or multiprogrammed workload, the real challenge is how to easily create *parallel software* to allow single programs to effectively exploit all of this raw performance potential. One promising technique fo ...

**Keywords:** Thread-level speculation, automatic parallelization, cache coherence, chip-multiprocessing

#### 4 Using the Alfa-1 simulated processor for educational purposes



Gabriel A. Wainer, Sergio Daicz, Luis F. De Simoni, Demian Wassermann

December 2001 **Journal on Educational Resources in Computing (JERIC)**, Volume 1 Issue 4

**Publisher:** ACM Press

Full text available: pdf(238.65 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Alfa-1 is a simulated computer designed for computer organization courses. Alfa-1 and its accompanying toolkit allow students to acquire practical insights into developing hardware by extending existing components. The DEVS formalism is used to model individual components and to integrate them into a hierarchy that describes the detailed behavior of different levels of a computer's architecture. We introduce Alfa-1 and the toolkit, show how to extend existing components, and describe how ...

**Keywords:** DEVS formalism, modeling computer architectures, systems specification

#### 5 Bibliography of recent publication in computer networking



July 1989 **ACM SIGCOMM Computer Communication Review**, Volume 19 Issue 3

**Publisher:** ACM Press

Full text available: pdf(2.53 MB) Additional Information: [full citation](#), [index terms](#)

#### 6 Evaluation of remote backup algorithms for transaction-processing systems



Christos A. Polyzois, Héctor García-Molina

September 1994 **ACM Transactions on Database Systems (TODS)**, Volume 19 Issue 3

**Publisher:** ACM Press

Full text available: pdf(1.87 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [citations](#), [index terms](#), [review](#)

A remote backup is a copy of a primary database maintained at a geographically separate location and is used to increase data availability. Remote backup systems are typically log-based and can be classified into 2-safe and 1-safe, depending on whether transactions commit at both sites simultaneously or first commit at the primary and are later propagated to the backup. We have built an experimental database system on which we evaluated the performance of the epoch and the dependency recons ...

**Keywords:** disaster recovery, hot spare, hot standby, remote backup

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| <a href="#">#10</a> | (((((least recently used) or LRU<IN>metadata)) <AND> (((cache and (poll* or monitor*))<in>metadata))) <AND> ((timestamp<IN>metadata))) |



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